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INFORMATION DISCLOSURE STATEMENT BY APPLICANT (use as many sheets as necessary)			Application Number	Unknown
			Filing Date	Herewith
			First Named Inventor	Mark A. Lemkin
			Group Art Unit	Unknown
			Examiner Name	Unknown
			Attorney Docket Number	AIMI-01924US0
Sheet	2	of	3	

OTHER PRIOR ART – NON PATENT LITERATURE DOCUMENTS			
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JT		WEY et al., An Improved Correlated Double Sampling Circuit for Low Noise Charge-Coupled Devices, IEEE Transactions on Circuits & Systems, Vol. 37, No. 12, Dec. 1990, pp. 1559-1565 •	
JT		WONGKOMET et al., Correlated Double Sampling in Capacitive Position Sensing Circuits for Micromachined Applications, 1998 IEEE, pp. 723-726 •	
JT		WANG et al., New High Precision Circuits for On-Chip Capacitor Ratio Testing and Sensor Readout, 1998 IEEE, pp. 1-547-1550 •	
JT		BOSER, Electronics for Micromachined Inertial Sensors, Transducers '97, 1997 Int'l Conference on Solid-State Sensors and Actuators, Chicago, June 16-19, 1997, pp. 1169-1172 •	
JT		KEMP et al., An Accelerometer Interface Circuit, IEEE 1995 Custom Integrated Circuits Conf., pp. 345-348 •	
JT		GERARD et al., An Accurate Low-Cost Capacitive Absolute Angular-Position Sensor with a Full-Circle Range, IEEE Transactions on Instrumentation & Measurement, Vol. 45, No. 2, Apr. 1996, pp. •	
JT		DEGRAUWE et al., "A Micropower CMOS-Instrumentation Amplifier", IEEE JSCC, pp. 805-807, June 1985 •	
JT		LEE, "A 4-Channel, 18b Sigma-Delta Modulator IC With Chopped-Offset Stabilization", ISSCC, pp. 238-39, 1996 •	
JT		RAZAVI, "Principles of Data Conversion System Design", IEEE Press, 1995, pp. 198-211 •	
JT		RANGANATHAN et al., Sub-Femtofarad Capacitive Sensing for Microfabricated Transducers Using Correlated Double Sampling & Delta Modulation, IEEE Transactions on Circuits & Systems-II: Analog and Digital Signal Processing, Vol. 47, No. 11, November 2000, pp. 1170-1176 •	

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